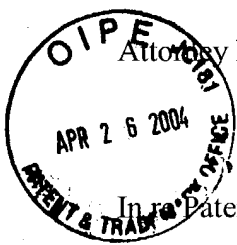


2127



Attorney Docket No. 42P10807

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Erik Cota-Robles et al.

Application No. 09/931,072

Filed: August 15, 2001

For: **TRACKING OPERATION  
SYSTEM PROCESS AND  
THREAD EXECUTION AND  
VIRTUAL MACHINE  
EXECUTION IN HARDWARE  
OR IN A VIRTUAL MACHINE  
MONITOR**

Examiner: Lillian Vo

Art Unit: 2127

**RECEIVED**

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Technology Center 2100

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered, and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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| I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below. |                        |
| By: <u>Anne Collette</u><br>Anne Collette  | Date: <u>4/22/2004</u> |

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

This Information Disclosure Statement is being submitted pursuant to one of the following (as indicated by an "X" to the left of the appropriate paragraph):

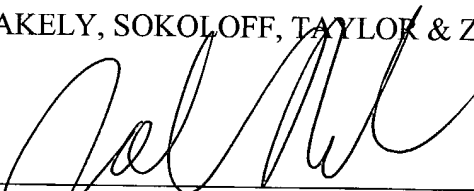
- X   37 C.F.R. §1.97(b).
- 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 9/22/89

  
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APR 26 2004

|                        |                  |
|------------------------|------------------|
| Application Number     | 09/931,072       |
| Filing Date            | August 15, 2001  |
| First Named Inventor:  | Erik Cota-Robles |
| Group Art Unit         | 2127             |
| Examiner Name          | Lillian Vo       |
| Attorney Docket Number | 42P10807         |

|       |   |   |   |
|-------|---|---|---|
| Sheet | 1 | 1 | 1 |
|-------|---|---|---|

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| Exam.<br>Initial* | Cite<br>No. <sup>1</sup> | Foreign Patent Document |                     |                                      | Name of Patentee or Applicant<br>of Cited Document | Date of<br>Publication of<br>Cited Document<br>MM-DD-YYYY | Pages, Columns,<br>Lines, Where Relevant<br>Passages or Relevant<br>Figures Appear | T <sup>6</sup> |
|-------------------|--------------------------|-------------------------|---------------------|--------------------------------------|--|---|--|----------------|
|                   |                          | Office <sup>3</sup>     | Number <sup>4</sup> | Kind Code <sup>5</sup><br>(If known) |  |   |  |                |
|                   |                          | EP                      | 0 961 193           | A2                                   | Texas Instruments Inc.                             | 12-01-1999  |  |                |
|                   |                          | EP                      | 0 965 902           | A2                                   | National Semiconductor Corp.                       | 12-22-1999  |  |                |
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|                   |                          | WO                      | 00/21238            |                                      | Intel Corporation                                  | 04-13-2000  |  |                |
|                   |                          | WO                      | 01/63994            | A2                                   | Iridian Technologies, Inc.                         | 08-30-2001  |  |                |

| Examiner Initials* | Cite No <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published | T <sup>2</sup> |
|--------------------|----------------------|--|----------------|
|                    |                      | INTEL CORPORATION, "IA-64 System Abstraction Layer Specification", Intel Product Specification, Order Number 245359-001, 112 pages, January 2000.  |                |
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Date Considered

<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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